PV.P SIDDHARTHA INSTITUTE OF TECHNOLOGY (Autonomous) KANURU, VIJAYAWADA

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

STUDENT ACTIVITIES 2017 - 2018

i. 17 Students qualified **Certified Lab-VIEW Associate Developer (CLAD)** exam conducted on 19th July, 2017.

S.No.	Name Of the Student
1	Padala Anil Kumar
2	Chowtapalli Anudeep
3	Arigela Avinash
4	Deevi Baby Sai Manasa
5	Dadisetty Bharath
6	Korini Bharathi
7	Garapati Harini
8	Desiboyina Jaya Sree
9	Dugginapalli Mounika
10	Venthrumilli Naga Lakshmi
11	Annabathula Revanth Pavan
12	Yarlagadda Sai Krishna
13	Jetty Sai Sirisha
14	Annamaraju Sasank
15	Duba Sravani
16	Arasavilli Venkata Naga Rupa
17	Sanapala Vinay

- ii. Ms.G.Lohitha, III B.Tech., ECE won I Prize in Essay writing competition at Gora Science Exhibition Centre held on 26th July, 2017.
- iii. **Mr Siva Naga Sai,** III B.Tech., ECE **won III Prize** in Essay writing competition at Gora Science Exhibition Centre held on 26th July, 2017.
- iv. **Ms.Gundabattula Hanitha Devi,** Regd. No: 16501A0436 won II prize in Debate competition at PB Siddhartha (LIMES-2K-17) on 15th December 2017.

- v. **Ms. B. Divya,** Regd. No: 14501A0408 won 1st Runner-up prize for **"Ms Andhra-2018"**Competition held on 27th January, 2018 by Satish Addala Creative Events at Lotus Food City, Vijayawada.
- vi. **Mr. M.R.K. Siva Naga Sai,** Regd. No: 15501A0462 won 3rd prize in Technical Quiz held on 9th February, 2018. (SITAR 2K18).
- vii. **Mr. M.R.K. Siva Naga Sai,** Regd. No: 15501A0462 won 1stPrize in Technical Quiz organized at VRSEC on the eve of "**AFOSEC-2018**" on 15th February, 2018.

Presentation/Participation in Seminars/Conferences/Workshops:

i. Dr.P.Rajesh Kumar, Professor & HOD mentored project "Design and Development of a smart cane for visually challenged" was selected for 1st AICTE – ECI CHAATRA VISWAKARMA AWARDS.

III B.Tech. ECE- Students:

- i. K.Charishma
- ii. P. Mangala Gouri,
- iii. N. Ratna Mythili
- iv. D.K.N.P.Sai Keerthana





ii. IV.B.Tech students won - Online Project Contest "DRISHTI" and received Project Kits from Edge Gate Technologies Pvt. Ltd., Bangalore.

Students: i. Mr. Yadunandan.

ii. Mr. Maneesh Yaday

- iii. **Mr. M.R.K. Siva Naga Sai,** Regd. No: 15501A0462presented a paper at RVRJC Fest on 5th& 6th January, 2018.
- iv. **Mr. M.R.K. Siva Naga Sai,** Regd. No: 15501A0462presented a paper at MIC College, Kachikacherla on 26th February, 2018.